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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/985,743	11/06/2001	Ryosuke Usui	57810-025	4232
75	90 01/25/2005		EXAM	INER
McDERMOTT, WILL & EMERY			LEE, EUGENE	
600 13th Street,	, N.W.			
Washington, D	C 20005-3096	ART UNIT		PAPER NUMBER
			2815	

DATE MAILED: 01/25/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

			IN
	Application No.	Applicant(s)	
	09/985,743	USUI ET AL.	
Office Action Summary	Examin r	Art Unit	
	Eugene Lee	2815	
The MAILING DATE of this communication a	_	1	<u> </u>
Period for Reply			
A SHORTENED STATUTORY PERIOD FOR REF THE MAILING DATE OF THIS COMMUNICATION  - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a r  - If NO period for reply is specified above, the maximum statutory perions  - Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the mail earned patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a eply within the statutory minimum of the dwill apply and will expire SIX (6) MO ute, cause the application to become A	reply be timely filed irty (30) days will be considered timely. NTHS from the mailing date of this commun ABANDONED (35 U.S.C. § 133).	ication.
Status			
1) Responsive to communication(s) filed on 21	December 2004.		
2a) ☐ This action is <b>FINAL</b> . 2b) ☑ The	nis action is non-final.		
3) Since this application is in condition for allow	vance except for formal ma	tters, prosecution as to the mer	its is
closed in accordance with the practice under	r <i>Ex parte Quayle</i> , 1935 C.	D. 11, 453 O.G. 213.	
Disposition of Claims			
4) Claim(s) 1-3,6 and 18 is/are pending in the a	application.		
4a) Of the above claim(s) is/are withd	•		
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>1-3,6 and 18</u> is/are rejected.		•	
7) Claim(s) is/are objected to.			
8) Claim(s) are subject to restriction and	I/or election requirement.		
Application Papers			
9)☐ The specification is objected to by the Exami	ner.		
10) The drawing(s) filed on is/are: a) a	ccepted or b) abjected to	by the Examiner.	
Applicant may not request that any objection to the	ne drawing(s) be held in abeya	ince. See 37 CFR 1.85(a).	
Replacement drawing sheet(s) including the corre	•		
11) The oath or declaration is objected to by the	Examiner. Note the attache	ed Office Action or form PTO-15	52.
Priority under 35 U.S.C. § 119			
<ul> <li>12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority docume</li> <li>2. Certified copies of the priority docume</li> <li>3. Copies of the certified copies of the priority</li> </ul>	ents have been received. ents have been received in a	Application No	e
application from the International Bure			
* See the attached detailed Office action for a li	st of the certified copies no	t received.	
Attachment(s)			
Notice of References Cited (PTO-892)		Summary (PTO-413)	
<ul> <li>D) Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>D) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/0</li> </ul>		(s)/Mail Date Informal Patent Application (PTO-152)	•
Paper No(s)/Mail Date	6) Other: _		

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## **DETAILED ACTION**

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## Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 12/21/04 has been entered.

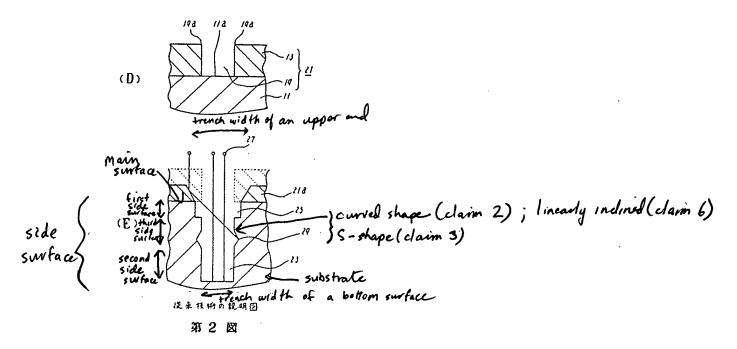
## Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1 thru 3, 6, and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Okabe et al. JPO 62-252139 in view of Kameyama 4,472,240. Okabe discloses (see, for example, figure 2 (E)) an element separation area (semiconductor device) comprising a semiconductor substrate 11, and element isolation trench 23. On page 3, lines 15-18 of the translation, Okabe discloses a trench of selected depth formed on a semiconductor substrate for a trench created around an element being filled with an insulation part to provide an element isolation part. The trench width of an upper end, trench width of a bottom surface, side surface, first side surface, second side surface and third side surface are identified in the figure below (FIG. 2 (E) of Okabe JPO 62-252139). The trench width of an upper end is larger than the

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trench width of a bottom surface. The length of the side surface located between the upper end and bottom end is larger than a straight line connected the upper end and bottom end. The first side surface and second side surface are perpendicular to the substrate and are connected by an inclined third side surface.



Okabe does not disclose said element isolation trench being substantially filled with an insulator. However, Kameyama discloses (see, for example, FIG. 11D) an element isolation region comprising an element isolation region 511a. The element isolation region is made from a SiO<sub>2</sub> film (insulator) 510a and separates adjacent semiconductor devices from each other. It would have been obvious to one of ordinary skill in the art at the time of invention to have said element isolation trench being substantially filled with an insulator in order to isolate semiconductor devices from each other to form an IC or LSI.

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Response to Arguments

4. Applicant's arguments with respect to claims 1-3, 6, and 18 have been considered but are

moot in view of the new ground(s) of rejection.

INFORMATION ON HOW TO CONTACT THE USPTO

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Eugene Lee whose telephone number is 571-272-1733. The

examiner can normally be reached on M-F 8-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Tom Thomas can be reached on 571-272-1664. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Eugene Lee

January 20, 2005

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